AC-2949
First Year B. Sc. (Sem. II) Examination
April / May - 2015
Applied Electronics : Paper - II
(Logic Circuits)

Time : 2 Hours] [Total Marks : 50

Instructions :
(1) Fill up strictly the details of signs on your answer book.
Name of the Examination :
First Year B. Sc. (Sem. II)
Name of the Subject :
Applied Electronics - II (Logic Circuits)

(2) Question no. 1 is compulsory.
(3) Symbols and terminology used here their usual meanings.

Q-1 Write short answers [8]

1. What is ROM?
2. What is D-flip-flop?
3. What is Shift Register?
4. What is Synchronous counter?

Q-2 1. Draw & discuss the "Edge triggered J-K flip flop" with suitable diagrams. 8
2. Draw & discuss the Edge triggered T- flip flop with suitable diagrams. 6

OR

Q-2 1. Discuss the DRAM in detail. 8
2. Discuss the types of ROMs in detail. 6

Q-3 1. Draw & discuss the Two Bit ripple up-down counter using negative triggered flip flop with suitable diagrams. 8
2. Draw & discuss the: MOD-10Asynchronous counter” using T flip flops with suitable diagrams. 6

OR
1. Draw & discuss the Serial In parallel Out Shift Register with suitable diagrams.
2. Draw & discuss the: Synchronous 3 bit Up Counter with suitable diagrams.

Q-4 Write short notes (Any Two)
1. Digital Comparator
2. BCD Adder
3. Ring Counter
4. Controlled buffer Register