AC-2951
First Year B. Sc. (Sem. II) Examination
April / May - 2015
Electronics for Computer Science
Logic Circuits : Paper - II

Time : 2 Hours] [Total Marks : 50

Instructions :
(1) Fill up strictly the details of signs on your answer book.
Name of the Examination : First Year B. Sc. (Sem. II)
Name of the Subject : Electronics for Comp. Sci. : Logic Circuits : Paper - II
Subject Code No. 2 9 5 1 Section No. (1,2,......) Nil
Seat No.

(2) Question no. 1 is compulsory.
(3) Symbols and terminology used here have their usual meanings.

Q-1 Write short answers [8]

1. What is RAM?
2. What is T-flip-flop?
3. What is Shift Register?
4. What is Asynchronous counter?

Q-2 1. Draw & discuss the Edge triggered J-K flip flop with suitable diagrams.  8
2. Draw & discuss the R-S - flip flop with suitable diagrams.  6

OR

Q-2 1. Discuss the DRAM in detail.  8
2. Discuss the types of ROM & EPROM in detail.  6

Q-3 1. Draw & discuss the Two Bit ripple up-down counter using negative triggered flip flop with suitable diagrams.  8
2. Draw & discuss the: MOD-10 Asynchronous counter using T flip flops with suitable diagrams.  6

OR
Q-3  1. Draw & discuss the "Parallel In parallel Out Shift Register" with suitable diagrams.
    2. Draw & discuss the: Synchronous 3 bit down counter with suitable diagrams.

Q-4  Write short notes (Any Two)
    1. Digital clock
    2. 4 Bit parallel binary Adder
    3. Ring Counter
    4. Controlled Invertor