



AC-3066
Second Year B. Sc. (Sem. IV) Examination
April / May – 2015
Applied Electronics : Paper - IV
(Advance Microprocessor)

Time : 2 Hours]

[Total Marks : 50

Instructions :

(1)

<p>नीचे दृशायेव निशानीवाणी विगतो उत्तरवडी पर अवश्य लपवी. Fillup strictly the details of signs on your answer book.</p> <p>Name of the Examination : SECOND YEAR B. SC. (SEM. IV)</p> <p>Name of the Subject : APPLIED ELECTRONICS : PAPER - IV</p> <p>Subject Code No. : 3 0 6 6 Section No. (1, 2,.....): Nil</p>	<p>Seat No. : <input type="text"/><input type="text"/><input type="text"/><input type="text"/><input type="text"/><input type="text"/></p> <div style="border: 1px solid black; border-radius: 15px; padding: 10px; text-align: center; width: 100%;">Student's Signature</div>
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- (2) Figures on the right indicate full marks
- (3) All symbols and abbreviations have their usual meaning.
- (4) Non-programmable calculators are allowed.
- (5) Q.1 is compulsory.
- (6) Assume data, if necessary.

- 1 Answer in short: 8
- (a) What is meant by Maskable interrupts?
 - (b) What is the position of the Stack Pointer after the PUSH instruction?
 - (c) What is stack?
 - (d) What is Instruction Set?

- 2 (a) Explain the instruction format of 8086 for data transfer instructions. 10
- (b) Explain Instruction: 4
- (i) AAS (ii) IN AL,DX

OR

- 2 (a) Explain memory organization of 8086. 10
- (b) Write an 8086 ALP to add two 16-bits in CX and DX and store the result in location 0500H addressed by DI. 4

- 3 (a) What is the purpose of segment registers in 8086? 10
(b) Explain Pins of 8086: 4
(i) NMI (ii) READY
- OR**
- 3 Describe Intel 8086 Microprocessor Architecture. 14
- 4 Write short notes on : **(any two)** 14
(a) Control and command word of 8259
(b) Rotate Instructions
(c) Immediate Addressing Mode
(d) Subroutine.
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