



AC-3306

Second Year B. Sc. (Electronics) (Sem. IV) Examination

March/April – 2015

Paper : Generic Elective

(Circuit Simulation)

Time : 2 Hours]

[Total Marks : 50

Instructions :

(1)

नीचे इशारेक निशानीवाणी विगतो उत्तरवही पर अवश्य कपवी. Fillup strictly the details of signs on your answer book.	Seat No. :
Name of the Examination :	<input type="text"/>
SECOND YEAR B. Sc. (ELECTRONICS) (SEM. 4)	<input type="text"/>
Name of the Subject :	<input type="text"/>
PAPER : GENERIC ELECTIVE	<input type="text"/>
Subject Code No. : <input type="text" value="3"/> <input type="text" value="3"/> <input type="text" value="0"/> <input type="text" value="6"/>	Section No. (1, 2,.....): <input type="text" value="Nil"/>
Student's Signature	

- (2) All questions are compulsory
- (3) Question- -1 carries 10 marks
- (4) Questions -2 and 3 carry 15 marks each
- (5) Question -4 carries 10 marks
- (6) Symbols and terminology used here have their usual meanings.
- (7) Scientific calculator is allowed.
- (8) Mobile (Cell phones)are strictly prohibited.

1 Write short answers : [10]

- (1) What is Snap –to-Grid and what is snap-to-pin? 2
- (2) Define Importing and Exporting in Microsim schematic? 2
- (3) What is use of Mocrosim schematic? 2
- (4) Differentiate between PADS and PADSTACKS 2
- (5) Define probe. 2

2 Answer the following : 15

- (1) Discuss about the "AKO creating and AKO editing " 8
in your own words
- (2) Explain about the Creating and Editing Pin and 7
Padstacks for Schematic.

OR

- 2 (1) Discuss about drawing a Schematic. 8
(2) How and where will you use Design Manager for schematic. 7
- 3 Answer the following : 15
(1) Discuss about the " Schematic Editor ". 8
(2) Explain about the creating and Editing hierarchical design for schematic. 7
- OR**
- 3 (1) Discuss about the Post processing for PCB. 8
(2) Explain the role of footprints for PCB. 7
- 4 Write short notes : **(any two)** 10
(1) ECO process
(2) Microsim Footprint library
(3) Symbol Editor for Microsim Schematic
(4) Stimulus editor.
-