DF-2998
Second Year B. Sc. (Sem. III) Examination
March/April – 2016
Applied Electronics : Paper - IV
(Microprocessor Circuit & Application)

Time : 2 Hours] [Total Marks : 50

Instructions :

(1)

(2) All 28 questions are compulsory.
(3) Symbols and terminology used in the paper have their usual meaning.
(4) Figures to right indicate full marks.
(5) Scientific calculator is allowed.
(6) Mobile (cell phone) are strictly prohibited.

Q. 1 to 12 Multiple choice questions : (1 mark)
Q. 13 to 22 Multiple Choice Questions : (2 marks)
Q. 23 to 28 Multiple Choice Questions : (3 marks)

O.M.R. Sheet बच्चा अंग्रेजी अवश्य चूंकि O.M.R. Sheet की पृष्ठ अपेक्षित की है।
Important instructions to fillup O.M.R. Sheet are given on back side of the provided O.M.R. Sheet.
1. An accumulator is working as
   (A) None of these
   (B) General purpose pad
   (C) Specific processing unit
   (D) Both of these

2. ROM stand for
   (A) None of these
   (B) Random only memory
   (C) Read only memory
   (D) Both of these

3. The unit which provides the necessary timing and control signals to the operations in microcomputer is
   (A) None of these
   (B) Central Processing Unit
   (C) Timing Unit
   (D) Control Unit

4. Programmable peripheral interface is
   (A) None of these
   (B) 8085
   (C) 8051
   (D) 8255
5 BSR stands for
   (A) None of these
   (B) Bit set reset
   (C) Bus set reset
   (D) Battery set reset

6 The full form of ALU is
   (A) None of these
   (B) Automatic Logic Unit
   (C) Arithmetic and Logic Unit
   (D) All Logic Unit

7 The 8085 is a micro-processor having
   (A) 32 Bits
   (B) 4 Bits
   (C) 16 Bits
   (D) 8 Bits

8 The physical components of system is called
   (A) None of these
   (B) Program
   (C) Hardware
   (D) Software
9 The 8085 microprocessor has

(A) All of these
(B) Control Bus
(C) Higher Order Address Bus
(D) Multiplex Bus

10 The full form of ALE is:

(A) None of these
(B) Addressed Latch Enable
(C) Automatic Linear Electronics
(D) Both of these

11 Multiplex bus is

(A) None of these
(B) Unidirectional
(C) By directional
(D) Multidirectional

12 A semi conducted device made by LSI technique with ALU, register array and control circuit in single chip is

(A) None of these
(B) Micro controller
(C) Micro computer
(D) Micro processor
13 ANA B will performed
   (A) None of these
   (B) AND operation between (B) and (B)
   (C) AND operation between (A) and (B)
   (D) Both of these

14 If (B)=11 H and if (C)=22 H then what will be (C) after executing the instruction MOV C, B
   (A) None of these
   (B) 11 H
   (C) 22 H
   (D) Both of these

15 If (C)=10 H then what will be the (C) after executive the instruction INR C
   (A) None of these
   (B) 13 H
   (C) 09 H
   (D) 11 H

16 LXI H will initiate
   (A) None of these
   (B) BC pair
   (C) HL pair
   (D) Both of these

17 The instruction used to inter change the contain of HL pair and DE pair =
   (A) None of these
   (B) XCHG
   (C) EX-CHANGE
   (D) XRA
18 \[ \text{XRA A =} \]
(A) None of these
(B) 11
(C) 00
(D) Both of these

19 \[ \text{If (A) = 10H and (B) = B1H then A+B =} \]
(A) None of these
(B) 11H
(C) B2H
(D) C1

20 \[ \text{The once compliment of 42H =} \]
(A) None of these
(B) ABH
(C) 24H
(D) BCH

21 \[ \text{The two’s compliment of 10H is} \]
(A) None of these
(B) F0 H
(C) A2 H
(D) 01 H

22 \[ \text{If (A)=B6 H and (C)=A2 H then A-B =} \]
(A) None of these
(B) 10 H
(C) 55 H
(D) 14 H
23 If the (A) = 62 H and (B) = 10 H then, what will be (A) after executing
instruction A+B and then A-B

(A) 62 H

(B) 12 H

(C) 26 H

(D) 82 H

24 What will be the twos compliment of register B if (B) = 55H

(A) DA H

(B) AB H

(C) BC H

(D) CA H

25 To clear an accumulator, one can use e

(A) None of these

(B) XRA A

(C) MVI A, 00 H

(D) Any of these

[ Contd...]
26. What will be the content of an accumulator after executing the following instructions - ANA B then ORA A, if (A)=11 H and (B)=22 H

(A) 11 H
(B) 72 H
(C) 01 H
(D) 22 H

27. What will be the (A) after executing A+B+C if (A)=00 H, (B) and (C)=11 H

(A) None of these
(B) 72 H
(C) C7 H
(D) 7C H

28. If (A)=23 H and (B) = 70E H then, what will be the (A) after executing instruction ORA B

(A) None of these
(B) 22 H
(C) 37 H
(D) 73 H