Instructions:

(1) Fill up strictly the details of signs on your answer book.

(2) All 28 questions are compulsory.

(3) Symbols and terminology used in the paper have their usual meaning.

(4) Figures to right indicate full marks.

(5) Scientific calculator is allowed.

(6) Mobile (cell phone) are strictly prohibited.

Q. 1 to 12 Multiple choice questions : (1 mark)
Q. 13 to 22 Multiple Choice Questions : (2 marks)
Q. 23 to 28 Multiple Choice Questions : (3 marks)

O.M.R. Sheet अनेकांश अनुसारी सूचनाओं आधार पर O.M.R. Sheet भरने हेतु अनुरोध है।

Important instructions to fillup O.M.R. Sheet are given on back side of the provided O.M.R. Sheet.
1. The 8085 microprocessor has
   (A) Multiplex Bus
   (B) All of these
   (C) Control Bus
   (D) Higher Order Address Bus

2. The full form of ALE is:
   (A) Both of these
   (B) None of these
   (C) Addressed Latch Enable
   (D) Automatic Linear Electronics

3. Multiplex bus is
   (A) Multidirectional
   (B) None of these
   (C) Unidirectional
   (D) By directional

4. A semi conducted device made by LSI technique with ALU, register array and control circuit in single chip is
   (A) Micro processor
   (B) None of these
   (C) Micro controller
   (D) Micro computer
5 An accumulator is working as
   (A) Both of these
   (B) None of these
   (C) General purpose pad
   (D) Specific processing unit

6 ROM stand for
   (A) Both of these
   (B) None of these
   (C) Random only memory
   (D) Read only memory

7 The unit which provides the necessary timing and control signals to the operations in microcomputer is
   (A) Control Unit
   (B) None of these
   (C) Central Processing Unit
   (D) Timing Unit

8 Programmable peripheral interface is
   (A) 8255
   (B) None of these
   (C) 8085
   (D) 8051
9 BSR stands for
   (A) Battery set reset
   (B) None of these
   (C) Bit set reset
   (D) Bus set reset

10 The full form of ALU is
   (A) All Logic Unit
   (B) None of these
   (C) Automatic Logic Unit
   (D) Arithmetic and Logic Unit

11 The 8085 is a micro-processor having
   (A) 8 Bits
   (B) 32 Bits
   (C) 4 Bits
   (D) 16 Bits

12 The physical components of system is called
   (A) Software
   (B) None of these
   (C) Program
   (D) Hardware
13 LXI H will initiate
   (A) Both of these
   (B) None of these
   (C) BC pair
   (D) HL pair

14 The instruction used to inter change the contain of HL pair and DE pair =
   (A) XRA
   (B) None of these
   (C) XCHG
   (D) EX-CHANGE

15 XRA A =
   (A) Both of these
   (B) None of these
   (C) 11
   (D) 00

16 If (A) = 10H and (B) = B1H then A+B =
   (A) C1
   (B) None of these
   (C) 11H
   (D) B2H

17 The once compliment of 42H =
   (A) BCH
   (B) None of these
   (C) ABH
   (D) 24H
18 The two’s compliment of 10H is
   (A) 01 H
   (B) None of these
   (C) F0 H
   (D) A2 H

19 If (A)=B6 H and (C)=A2 H then A-B =
   (A) 14 H
   (B) None of these
   (C) 10 H
   (D) 55 H

20 ANA B will performed
   (A) Both of these
   (B) None of these
   (C) AND operation between (B) and (B)
   (D) AND operation between (A) and (B)

21 If (B)=11 H and if (C)=22 H then what will be (C) after executing the instruction MOV C, B
   (A) Both of these
   (B) None of these
   (C) 11 H
   (D) 22 H

22 If (C)=10 H then what will be the (C) after executive the instruction INR C
   (A) 11 H
   (B) None of these
   (C) 13 H
   (D) 09 H
23. To clear an accumulator, one can use e

(A) Any of these

(B) None of these

(C) XRA A

(D) MVI A, 00 H

24. What will be the content of an accumulator after executing the following instructions - ANA B then ORA A, if (A)=11 H and (B)=22 H

(A) 22 H

(B) 11 H

(C) 72 H

(D) 01 H

25. What will be the (A) after executing A+B+C if (A)=00 H, (B) and (C)=11 H

(A) 7C H

(B) None of these

(C) 72 H

(D) C7 H
26 If (A) = 23 H and (B) = 70E H then, what will be the (A) after executing instruction ORA B

(A) 73 H
(B) None of these
(C) 22 H
(D) 37 H

27 If the (A) = 62 H and (B) = 10 H then, what will be (A) after executing instruction A+B and then A-B

(A) 82 H
(B) 62 H
(C) 12 H
(D) 26 H

28 What will be the tow’s compliment of register B if (B) = 55H

(A) CA H
(B) DA H
(C) AB H
(D) BC H