DF-2998
Second Year B. Sc. (Sem. III) Examination
March/April – 2016
Applied Electronics : Paper - IV
(Microprocessor Circuit & Application)

Time : 2 Hours] 
[Total Marks : 50

Instructions :

(1) Fill up strictly the details of your name and roll number in the OMR sheet.

Name of the Examination:
SECOND YEAR B. Sc. (SEM. 3)

Name of the Subject:
APPLIED ELECTRONICS - 4

Subject Code No.: 2998

Seat No.: (Student's Signature)

(2) All 28 questions are compulsory.
(3) Symbols and terminology used in the paper have their usual meaning.
(4) Figures to right indicate full marks.
(5) Scientific calculator is allowed.
(6) Mobile (cell phone) are strictly prohibited.

Q. 1 to 12 Multiple choice questions : (1 mark)
Q. 13 to 22 Multiple Choose Questions : (2 marks)
Q. 23 to 28 Multiple Choice Questions : (3 marks)

O.M.R. Sheet कस्तूर अंकांत अन्य अन्यको सुनामो आवेद
O.M.R. Sheet नी पाठ्रेका अन्त्य 

Important instructions to fillup O.M.R. Sheet are given on back side of the provided O.M.R. Sheet.
1. Programmable peripheral interface is
   (A) 8051
   (B) 8255
   (C) None of these
   (D) 8085

2. BSR stands for
   (A) Bus set reset
   (B) Battery set reset
   (C) None of these
   (D) Bit set reset

3. The full form of ALU is
   (A) Arithmetic and Logic Unit
   (B) All Logic Unit
   (C) None of these
   (D) Automatic Logic Unit

4. The 8085 is a micro-processor having
   (A) 16 Bits
   (B) 8 Bits
   (C) 32 Bits
   (D) 4 Bits
5 The physical components of system is called

(A) Hardware
(B) Software
(C) None of these
(D) Program

6 The 8085 microprocessor has

(A) Higher Order Address Bus
(B) Multiplex Bus
(C) All of these
(D) Control Bus

7 The full form of ALE is :

(A) Automatic Linear Electronics
(B) Both of these
(C) None of these
(D) Addressed Latch Enable

8 Multiplex bus is

(A) By directional
(B) Multidirectional
(C) None of these
(D) Unidirectional
9 A semi conducted device made by LSI technique with ALU, register array and control circuit in single chip is

(A) Micro computer
(B) Micro processor
(C) None of these
(D) Micro controller

10 An accumulator is working as

(A) Specific processing unit
(B) Both of these
(C) None of these
(D) General purpose pad

11 ROM stand for

(A) Read only memory
(B) Both of these
(C) None of these
(D) Random only memory

12 The unit which provides the necessary timing and control signals to the operations in microcomputer is

(A) Timing Unit
(B) Control Unit
(C) None of these
(D) Central Processing Unit
13 The once compliment of 42H =
   (A)  24H
   (B)  BCH
   (C)  None of these
   (D)  ABH

14 The two’s compliment of 10H is
   (A)  A2 H
   (B)  01 H
   (C)  None of these
   (D)  F0 H

15 If (A)=B6 H and (C)=A2 H then A-B =
   (A)  55 H
   (B)  14 H
   (C)  None of these
   (D)  10 H

16 ANA B will performed
   (A)  AND operation between (A) and (B)
   (B)  Both of these
   (C)  None of these
   (D)  AND operation between (B) and (B)

17 If (B)=11 H and if (C)=22 H then what will be (C) after executing the
    instruction MOV C, B
   (A)  22 H
   (B)  Both of these
   (C)  None of these
   (D)  11 H
18 If \((C) = 10\) H then what will be the \((C)\) after executing the instruction INR C
(A) 09 H
(B) 11 H
(C) None of these
(D) 13 H

19 LXI H will initiate
(A) HL pair
(B) Both of these
(C) None of these
(D) BC pair

20 The instruction used to interchange the content of HL pair and DE pair =
(A) EX-CHANGE
(B) XRA
(C) None of these
(D) XCHG

21 XRA A =
(A) 00
(B) Both of these
(C) None of these
(D) 11

22 If \((A) = 10\) H and \((B) = B1\) H then \(A+B =
(A) B2H
(B) C1
(C) None of these
(D) 11H

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23 If (A)=23 H and (B) = 70E H then, what will be the (A) after executing instruction ORA B

(A) 37 H

(B) 73 H

(C) None of these

(D) 22 H

24 If the (A) = 62 H and (B) = 10 H then, what will be (A) after executing instruction A+B and then A-B

(A) 26 H

(B) 82 H

(C) 62 H

(D) 12 H

25 What will be the tow’s compliment of register B if (B) = 55H

(A) BC H

(B) CA H

(C) DA H

(D) AB H
26 To clear an accumulator, one can use e

(A) MVI A, 00 H

(B) Any of these

(C) None of these

(D) XRA A

27 What will be the content of an accumulator after executing the following instructions - ANA B then ORA A, if (A)=11 H and (B)=22 H

(A) 01 H

(B) 22 H

(C) 11 H

(D) 72 H

28 What will be the (A) after executing A+B+C if (A)=00 H, (B) and (C)=11 H

(A) C7 H

(B) 7C H

(C) None of these

(D) 72 H