



**DMM-3111**  
**Second Year B. Sc. (Sem. IV) Examination**  
**March / April - 2016**  
**Electronics : Generic Elective**  
**(Circuit Simulation)**

Time : 2 Hours]

[Total Marks : 50

**Instructions :**

(1)

<p>नीचे दृशावेल निशानीवाणी विगतो उत्तरवही पर अवश्य कभवी. Fillup strictly the details of signs on your answer book.</p> <p>Name of the Examination : ☛ <b>Second Year B. Sc. (Sem. IV)</b></p> <p>Name of the Subject : ☛ <b>Electronics : Generic Elective (Circuit Simulation)</b></p> <p>☛ Subject Code No. : <b>3 1 1 1</b> ☛ Section No. (1, 2,.....) : <b>NIL</b></p>	<p>Seat No. : <input type="text"/><input type="text"/><input type="text"/><input type="text"/><input type="text"/><input type="text"/><input type="text"/></p> <div style="border: 1px solid black; border-radius: 15px; padding: 10px; text-align: center; width: 100%;">Student's Signature</div>
--	---

- (2) Figure on the right indicates full marks.
- (3) All symbols and abbreviations have their usual meaning.
- (4) Non-programmable calculators are allowed.
- (5) Q. 1 is compulsory.
- (6) Assume data if necessary.

1 Answer in brief :

- |     |   |           |
|-----|---|-----------|
| (1) | Define the following :  | <b>12</b> |
|     | (a) Grid  |           |
|     | (b) Pspice  |           |
|     | (c) Attributes  |           |
|     | (d) Work Space  |           |
|     | (2) What do you mean by Probe?  | <b>2</b>  |
| 2   | (a) What is the meaning of symbol library? How a symbol is placed on the schematic page? Explain the meaning of part value. | <b>8</b>  |
|     | (b) Discuss different types of libraries that schematics can recognize?   | <b>4</b>  |

**OR**

- 2 (a) Explain AC sweep and noise. 8  
 (b) Define : 4  
 (a) Solder mask  
 (b) Pins and vias.

- 3 (a) What are the main functions of Microsim schematics? 7  
 Explain the basic drawing features for drawing a schematic.  
 (b) How can you change reference designators and part values? 5

**OR**

- 3 (a) How will you do drawing and labeling wires in Microsim schematics ? 6  
 (b) Write the steps for placing and labeling the port. 06
- 4 (a) What are the different ways to create and edit model? 8  
 (b) What is the method of placing a power ground in the circuit? 04

**OR**

- 4 (a) Using stimulus editor show how you will make a study of PN junction diode characteristics. 5  
 (b) Describe the method of creating schematic diagram of Kirchoff's current law circuit and simulate it. 7

