



DPP-2949
First Year B. Sc. (Sem. II) Examination
March / April - 2016
Applied Electronics : Paper - II
(Logic Circuits)

Time : 2 Hours]

[Total Marks : 50

Instructions :

(1)

<p>नीचे दशावेक निशानीवाणी विगतो उत्तरवही पर अवश्य दखवी. Fillup strictly the details of signs on your answer book.</p> <p>Name of the Examination : FIRST YEAR B. Sc. (SEM. 2)</p> <p>Name of the Subject : APPLIED ELECTRONICS - 2</p> <p>Subject Code No. : 2 9 4 9 Section No. (1, 2,.....) : NIL</p>	<p>Seat No. : <input type="text"/><input type="text"/><input type="text"/><input type="text"/><input type="text"/><input type="text"/><input type="text"/></p> <div style="border: 1px solid black; border-radius: 15px; padding: 10px; text-align: center; margin-top: 10px;">Student's Signature</div>
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- (2) All questions are compulsory.
(3) Figures to the right indicate marks.

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|---|---|---|
| 1 | Write short answers : | 8 |
| | (1) What is ROM ? | |
| | (2) What is J-K flip-flop ? | |
| | (3) What is Shift Register ? | |
| | (4) What is Synchronous counter ? | |
| 2 | (1) Draw and discuss the "Master slave J-K flip-flop" with suitable diagrams. | 8 |
| | (2) Draw and discuss the D-flip flop with suitable diagrams. | 6 |
- OR**
- | | | |
|---|---|---|
| 2 | (1) Discuss the ROM in detail. | 8 |
| | (2) Discuss the types of DRAMs in detail. | 6 |

3	(1) Draw and discuss the Ring counter in detail with suitable diagrams.	8
	(2) Draw and discuss the Binary counter with suitable diagrams.	6
OR		
3	(1) Draw and discuss the “Parallel In Parallel Out Shift Register” with suitable diagrams.	8
	(2) Draw and discuss the : "Synchronous 3 bit Up counter" with suitable diagrams.	6
4	Write short notes : (any two)	14
	(1) Digital Comparator	7
	(2) Binary Adder	7
	(3) Asynchronous Counter	7
	(4) Controlled buffer Register.	7
