



RAN-7031

S. Y. B.Sc. (Semester - IV) Examination

March / April - 2019

Electronics Paper - 4 (Old Course)

सूचना : / Instructions

(1)

नीचे दशविले निशानीवाणी विगतो उत्तरवही पर अवश्य लभवी.
Fill up strictly the details of signs on your answer book

Name of the Examination:
S. Y. B.Sc. (Semester - IV)

Name of the Subject :
Electronics Paper - 4 (Old Course)

Subject Code No.: 7 0 3 1

Seat No.:

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Student's Signature

- (2) Q. 1 is compulsory.
(3) Abbreviations have their usually meaning.

Q.1 Answer in Brief

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- A) What is the function of Accumulator in 8-bit operation and what is the function of HL register pair in 16-bit addition?
B) What do you understand by interrupt? State all interrupts available with 8085A and state their priorities.
C) Explain the instruction DAD rp, XRI 8-bit data
D) Explain the instructions CMP r and XCHG.

Q.2 A) A double precision number (a 16-bit unsigned number) is stored in 14

memory location X and X + 1. Another double precision number is stored at memory location Y and Y + 1. Write a microprocessor program to compute the sum of the two numbers and store the result at memory location Wand W+1. (DO NOT USE DAD Instruction)

- B) Write a program to find largest number from the three given numbers stored at some memory location.

OR

- Q.2 A)** Write a program to add series of 8-bit numbers stored at specific memory location and assume that the result is 8-bit only. **14**
- B) Explain, in detail, the function of HOLD and HLDK pin of 8085A with reference to other master like direct memory access controller.

- Q.3 A)** Explain, in detail, the architecture of 8085A.
- B) Write a program to find 1's complement and 2's complement of 8-bit data.

OR

- Q.3 A)** Two BCD numbers are stored at consecutive memory locations X and X + 1. Write a program to add these two number and store the result at memory location Y. (Ignore possible overflow)
- B) Write a program to subtract two 8-bit numbers and assume that the result is 8-bit only.
- Q.4 A)** Write a program to mask off bit number 0, 2, 4, and 7 of 8-bit numbers ($D_7 - D_0$ bit pattern).
- B) What do you understand by addressing mode? With suitable examples explain, in detail, all addressing mode of 8085A

OR

- Q.4 A)** One binary number is stored at memory location X. Write a microprocessor program to compute the number of its logical 1s and store the result at memory location Y.
- B) What do you understand by Stack and Stack pointer? Explain the register structure of 8085A.
