



## **DPP-2997**

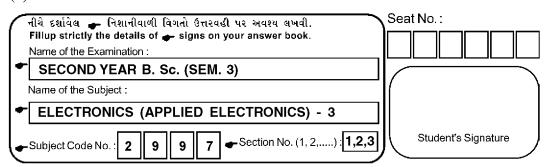
## Second Year B. Sc. (Sem. III) Examination March / April - 2016

Electronics (Applied Electronics): Paper - III (Electronics Devices & Circuit)

Time: 2 Hours [Total Marks: 50

## **Instructions:**

**(1)** 



- (2) All 28 questions are compulsory.
- (3) Symbols used in the paper have their usual meaning.
- (4) Figures to right indicate full marks.

Q. 1 to 12 Multiple Choice Questions: (1 mark)

Q. 13 to 22 Multiple Choice Questions: (2 marks)

Q. 23 to 28 Multiple Choice Questions: (3 marks)

O.M.R. Sheet ભરવા અંગેની અગત્યની સૂચનાઓ આપેલ O.M.R. Sheetની પાછળ છાપેલ છે.

Important instructions to fillup O.M.R. Sheet is given on back side of the provided O.M.R. Sheet.

1	Full	form of JFET
	(A)	Junction field effect transformer
	(B)	Joint field effect transformer
	(C)	Joint field effect transistor
	(D)	Junction field effect transistor
2	Full	form of MOSFET
	(A)	Metal oxygen semiconductor field effect transistor
	(B)	Metal oxygen semicondoctor field effect transformer
	(C)	Metal oxide semiconductor field effect transformer
	(D)	Metal oxide semiconductor field effect transistor
3	Full	form of CMOS
	(A)	Corrosive metal oxide semiconductor
	(B)	Corrospondent metal film oxide semiconductor
	(C)	Compulsory metal oxide semiconductor
	(D)	Complementary metal oxide semiconductor
4	Gain	-bandwidth product of amplifier with feedback and without feedback
	(A)	Both of these
	(B)	None of these
	(C)	Equal
	(D)	Unequal

5	Condition required for oscillation		
	(A)	Barcation criteria and negative feedback	
	(B)	Negative and positive feedback	
	(C)	Barkhausen criteria and positive feedback	
	(D)	Amplifier and negative feedback	
6	Full	form of UJT	
	(A)	Uni-junctional transistor	
	(B)	None of these	
	(C)	Uni joint transformer	
	(D)	Union junction transistor	
7	For	oscillator circuit	
	(A)	Input required, feedback not required	
	(B)	No input and feedback	
	(C)	Input and frequency determining network is required	
	(D)	No input, frequency determining network / tank circuit is required	
8	For	amplifier circuit	

- (A) No input required, feedback required
- (B) No input and feedback
- (C) Input and feedback network is required
- (D) Input required, no feedback required

9	In oscillator the negative feedback is used for		
	(A)	Stabilizing the output amplitude	
	(B)	Decreasing the output impedance	
	(C)	Increasing the output amplitude	
	(D)	Decreasing the output amplitude	
10	The	negative feedback is used in the amplifier -	
	(A)	For improving the gain stability	
	(B)	All of these	
	(C)	For extending the bandwidth	
	(D)	For increasing the impedance	
11	A cla	ass-C amplifier is operated with its operating point set in	
	(A)	Active region	
	(B)	None of these	
	(C)	Saturation region	
	(D)	Cut-off region	
12	Full	form of BJT	
	(A)	BI-Junction transformer	
	(B)	BI-polar junction transformer	
	(C)	BI-Junction transistor	
	(D)	BI-polar junction transistor	
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13	Whe	on the collector is at ac ground is called a grounded collector or amplifier, stepping up the impedence is the main reason for using
	$\overline{CC}$	amplifier, also known as
	(A)	Common emitter emitter follower
	(B)	Common collector, emitter-follower
	(C)	Emitter-Follower, Common collector
	(D)	Common base, emitter follower
14	amp	ac collector voltage is 180° out of face with the ac base voltage. This inversion between base and collector happens in all base driven lifiers. The phase of the emitter voltage is the same as the phase of voltage.  Base, Phase None of thse
	` ′	Phase, base
	(D)	Face, Phase
15	measinorm in(A) (B) (C) (D) The is im (A)	Maximum, Large Maximum, Less Less, Maximum  E-MOSFET operates in the mode only. This kind of MOSFET portant in digital circuit. It is also known as normally MOSFET. Enhancement, on
	(B)	Only enhancement, on
	(C)	Enhancement, off
	(D)	De-enhancement, off
17	If tra	ansistors $\alpha_{dc} = 0.98$ , the value of $\beta_{dc}$
	(A)	.049
	(B)	.0049
	(C)	49
	(D)	.49
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18	If transistors $\beta_{dc}$ =100, then value of $\alpha_{dc}$		
	(A) 9.9		
	(B) 99		
	(C) .099		
	(D) .99		
19	The $\alpha$ (dc alpha) of a transistor equal the ratio of current to current, and $\beta$ (dc Beta) equals the ratio of current to		
	current.		
	(A) Both of these		
	(B) None of the these		
	(C) Collector to emitter and collector to base		
	(D) Collector to base and collector to emitter		
20	If you reduce all ac sources to zero and open all capacitor, the circuit that remains is called equivalent circuit. If you reduce all sources to zero and short all coupling and by-pass capacitors, the circuit that remains is the equivalent circuit.  (A) Transient, steady  (B) Small signal, Large signal  (C) dc, ac  (D) ac, dc	)	
21	A by-pass capacitor is similar to coupling capacitor except that it couples an ungrounded points to a point. A by-pass capacitor produces an ac		
	(A) Supply, Ground		
	(B) Grounded, Supply		
	(C) Ground, Grounded		
	(D) Grounded, Ground		
22	You multiply individual $\beta$ 's to get the overall $\beta$ of a pair. If $\beta_1$ is		
	50 and $\beta_2$ is 100 then $\beta$ equals		
	(A) Coupling, 5000		
	(B) Decoupling, 5000		
	(C) Darlington, 500		
	(D) Darlington, 5000		

23	The	three part of a JFET is the source, the and the The
	field	effect is related to the layer around each pn junction. The
	more	negative the gate voltage, the the drain current.
	(A)	Gate, Drain, Depletion, Smaller
	(B)	Gate, Drain, Depletion, Larger
	(C)	Gate, Drain P-type, Smaller
	(D)	Gate, Drain, n-tye, Smaller
24	Data	sheet of JFET is $g_{\rm m} = 75 \mu s$ then what is $r_{\rm d}$ ?
	(A)	1330 $k\Omega$
	(B)	13.3 $k\Omega$
	(C)	133 $k\Omega$
	(D)	$1.33 k\Omega$
25 In JFET the change in drain current of 0.2 mA and corresponding 0.001V, then $\mathbf{g}_{\mathrm{m}}$ is		ET the change in drain current of 0.2 mA and corresponding change of $1\mathrm{V}$ , then $g_{\mathrm{m}}$ is
	(A)	$200\mu s$
	(B)	$20 \mu s$
	(C)	$0.0002~\mu s$
	(D)	$2000\mu s$

26	no ir	quicent collector current and voltage are the $I_{C}$ and $V_{CE}$ when there is nput You can determine quiescent current and voltage from the _ equivalent circuit. $V_{CEO}$ represent the collector to emitter voltage		
		ac signal.		
	(A)	Signal, ac, No		
	(B)	None of these		
	(C)	Signal, dc, No		
	(D)	Signal, ac, with		
27	Beca or th	use the gate is insulated from the channel, a mosfet is also known as FET. The D-MOSFET can operate in either the enhancement mode ne mode. This type of MOSFET is also known as normally MOSFET.		
	(A)	Insulated-Gate Depletion, On		
	(B)	Floating-gate, Depletion On and Insulated-Gate Depletion, On		
	(C)	Insulated-gate, ehnahcement, Off		
	(D)	Floating-gate, Depletion On		
28	is	The key difference between a JFET and a bipolar transistor is this: the gate is biased and whereas the base is biased. The crucial difference means the JFET is a controlled device.		
	(A)	Forward, Reverse, Voltage		
	(B)	Forward, Forward, Voltage		
	(C)	Forward, Reverse, Current		
	(D)	Reverse, Forward, Voltage		