



JB-3137

Second Year B. Sc. (Sem. III) Examination

March/April – 2013

Circuit Simulation

(I. D.)

Time : 3 Hours]

[Total Marks : 70

Instructions :

(1)

नीचे दशांशवैध निशानीवाणी विगतो उत्तरवही पर अवश्य कर्तवी. Fillup strictly the details of signs on your answer book.	Seat No. :
Name of the Examination :	<input type="text"/>
← S. Y. B. SC. (SEM. 3)	<input type="text"/>
Name of the Subject :	<input type="text"/>
← CIRCUIT SIMULATION	<input type="text"/>
← Subject Code No. : <input type="text"/> 3 <input type="text"/> 1 <input type="text"/> 3 <input type="text"/> 7 ← Section No. (1, 2,.....): <input type="text"/> Nil	<input type="text"/>
	Student's Signature

- (2) Questions to the extreme right indicates full marks
- (3) Question no 1 is compulsory
- (4) Draw diagrams wherever necessary

- 1 Write very short answers : 10
 - (i) What is Simulation ?
 - (ii) Explain the procedure of moving a part.
 - (iii) What is the meaning of reference designator?
 - (iv) What is the importance of design manager?
 - (v) What is the meaning of stimulus?
 - (vi) What is DC sweep analysis
 - (vii) What is a copper clad sheet
 - (viii) What is a grid
 - (ix) What is solder mask
 - (x) What is silk screen layer

- 2 (a) Explain how the Schematic page size is changed. 10
Explain the method of getting new component on the schematic page. What are part libraries?
- (b) Explain the method of changing the component value 5
and the reference designator.

OR

- 2 (a) What is the method of checking the Symbol library ? 10
How will you connect the various components on the schematic page
- (b) What is the method of placing a power and ground in the circuit what is its importance. 5
- 3 (a) Explain the part browser of Microsim design lab. Also explain the method of making connections of various components using the place wire tool 10
- (b) Explain the method of creating a new project using the design manager, used for study of the CE amplifier DC universal biasing circuit. 5
- OR**
- 3 (a) What is Probe utility of the circuit analysis in Microsim Design Lab 10
- (b) Using DC calculation method explain the analysis method of Kirchoff's current law. 5
- 4 (a) Explain the method of making a PCB. 10
- (b) What are PTH and via on a double sided PCB. 5
- OR**
- 4 (a) What are padstacks? Explain how will you create footprint which is not present in the library? 10
- (b) What is the meaning of auto routing ? 5
- 5 Write short notes : (any two) 15
- (i) Design Manager
- (ii) Editing footprints
- (iii) Forier Analysis
- (iv) Making a PCB.
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