



J-0874
Second Year B. Sc. Examination
March / April – 2013
Circuit Simulation
(IDS CAN Course For Electronics)

Time : Hours]

[Total Marks : 35

Instructions :

(1)

<p>नीचे दशांशिक निशानीवाणी विगतो उत्तरवही पर अवश्य कभवी. Fillup strictly the details of signs on your answer book.</p> <p>Name of the Examination : S. Y. B. Sc.</p> <p>Name of the Subject : Circuit Simulation</p> <p>Subject Code No. : 0 8 7 4 Section No. (1, 2,.....) : Nil</p>	<p>Seat No. : □ □ □ □ □ □</p> <p style="text-align: center; border: 1px solid black; border-radius: 15px; padding: 10px;">Student's Signature</p>
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- (2) Figures on the right indicates full marks.
- (3) All symbols and abbreviations have their usual meaning.
- (4) Non-programmable calculators are allowed.
- (5) Q.1 is compulsory.

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|---|--|----|
| 1 | (a) What is a drill chart ? | 08 |
| | (b) What is the importance of design manager ? | |
| | (c) What is AC sweep analysis ? | |
| | (d) What is grid ? | |
| 2 | (a) Explain the method of creating a new workspace using the Microsim design lab. How will you start the schematic design tool ? | 08 |
| | (b) Using the schematic design tool explain the method to draw the circuit of universal transistor biasing circuit. | 05 |

OR

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|---|---|----|
| 2 | (a) What is the methods of checking the Symbol library ? How the reference designator and the part values be changed? | 08 |
|---|---|----|

- (b) What is the method of placing a power and ground in the circuit ? What is its importance ? **05**
- 3** (a) What is Probe utility of the circuit analysis in Microsim Design Lab ? **08**
- (b) Using DC calculation method explain the analysis method of Kirchoff's current law. **06**

OR

- 3** (a) What is stimulus editor ? Explain the mehtod of DC sweep analysis. **08**
- (b) Using the stimulus editor show how will you make a study of diode characteristics. **06**
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